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| 10/669,648 | 09/25/2003 | Prasad Golla | Q77710 | 7770 | |
| 23373 7590 05/29/2008 SUGHRUE MION, PLLC 2100 PENNSYL VANIA AVENUE, N.W. | | | EXAM | EXAMINER | |
| | | | WONG, XAVIER S | | |
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The time period for reply, if any, is set in the attached communication.

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Remarks

The applicant argues that Blanquer does not teach "each server comprising a

scheduler module which is independent for each server" as recited in claim 1. The

examiner cited **Blanquer** because **Blanquer**'s scheduler schedules flows separately for

each scheduler and it would not place the claim in condition for allowance due to the

obviousness between centralized (Blanquer) versus decentralized (claimed) schedulers

for similar purpose of independently scheduling each server accordingly.

The applicant further argues that neither Blanquer nor Nemirovsky teach

"plurality of servers being associated with a respective one of the plurality of resources."

First, "resources" is not clearly defined in the claim language as to what the "resources"

perform. Further, the "queue dedicated to one or a set of resources" wherein when the

"resources" of Nemirovsky are implemented in between the output ports and

destinations (e.g. structurally via the resources) of **Blanquer**, they serve similar purpose of

prioritized scheduling since the priority of each stream determines instant access to

"resources" (Nemirovsky [0037]).

Xavier Szewai Wong X.S.W / x.s.w 22nd May 2008

/Seema S. Rao/

Supervisory Patent Examiner, Art Unit 2616